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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 9086

BUCHWALD et al.

Art Unit: 2634

Appl. No.: 09/844,441

Examiner: Zheng, Eva Y.

Filed: April 30, 2001

Atty. Docket: 1875.0560004

High-Speed Serial Data Transceiver and Related

Methods

Second Supplemental Information Disclosure Statement

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicants have checked the appropriate boxes below.

- ☐ 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- 2. Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. No statement or fee is required.
- - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than

three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- \[
 \infty c. Attached is our PTO-2038 Credit Card Payment Form in the amount of \$180.00 in payment of the fee under 37 C.F.R. §
 1.17(p).
- ☐ 4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Enclosed find our PTO-2038 Credit Card Payment Form in the amount of \$______ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
 - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than

three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- 5. The document(s) was/were cited in a search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- 6. A concise explanation of the relevance of the non-English language document(s) appears below in accordance with 37 C.F.R. § 1.98(a)(3).
- ∑ 7. Copies of the documents listed in the sections entitled 'Foreign Patent
 Documents' and 'Other Documents' are enclosed. However, in accordance
 with 37 C.F.R. § 1.98(a)(2), copies of the U.S. patents and patent application
 publications cited on the attached Form PTO-1449 are not enclosed.
- 8. Copies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No.______,

BUCHWALD *et al.* Appl. No. 09/844,441

- 5 -

filed _____, which is relied upon for an earlier filing date under 35

U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. §

1.98(d).

9. It is expected that the examiner will review the prosecution and cited art in

parent application no. _____ in accordance with MPEP 2001.06(b), and

indicate in the next communication from the office that the art cited in the

earlier prosecution history has been reviewed in connection with the present

application.

It is respectfully requested that the Examiner initial and return a copy of the

enclosed Form PTO-1449, and indicate in the official file wrapper of this patent

application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee

deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Patrick E. Garrett

Attorney for Applicants Registration No. 39,987

Date:

2/28/05

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600

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Page 1 of 10 ATTY, DOCKET NO. APPLICATION NO. **FORM PTO-1449** 09/844,441 1875.0560004 **INVENTORS** SECOND SUPPLEMENTAL BUCHWALD et al. NFORMATION DISCLOSURE STATEMENT FILING DATE **ART UNIT** April 30, 2001 2634 **U.S. PATENT DOCUMENTS** EXAMINER CLASS SUB-CLASS **FILING DATE** DATE NAME INITIAL DOCUMENT NUMBER 09/1996 AA1 5,554,945 Lee et al. 03/1997 Lee et al. AB1 5,614,855 12/1997 AC1 5,703,905 Langberg 10/1998 Cloke et al. AD1 5,822,143 AE1 5.945.862 08/1999 Donnelly et al. **FOREIGN PATENT DOCUMENTS** EXAMINER TRANSLATION DATE COUNTRY CLASS SUB-CLASS INITIAL DOCUMENT NUMBER ΕP EP 1 006 697 06/2000 AF1 N/A 10/2001 EΡ AG1 EP 1 139 619 N/A 04/2001 WO WO 01/29991 AH1 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Black, Jr., W. and Hodges, D., "Time Interleaved Converter Arrays," IEEE Journal of Solid-State AI1 Circuits, IEEE, Vol. SC-15, No. 6, December 1980, pages 1022-1029. Conroy, C. et al., "An 8-b 85-MS/s Parallel Pipeline A/D Converter in 1-µm CMOS," IEEE Journal of AJ1 Solid-State Circuits, IEEE, Vol. 28, No. 4, April 1993, pages 447-454. Dally, W. and Poulton, J., "Transmitter Equalization for 4Gb/s Signalling," Proceedings of Hot AK1 Interconnects IV, Palo Alto, CA, 1996, 10 pages. Ellersick, W. et al., "A Serial-Link Transceiver Based on 8GSample/s A/D and D/A Converters in 0.25µm CMOS," IEEE International Solid-State Circuits Conference, IEEE, 2001, page 58-59 and AL1 430. Ellersick, W. et al., "GAD: A 12-GS/s CMOS 4-bit A/D Converter for an Equalized Multi-Level Link," AM1 Symposium on VLSI Circuits Digest of Technical Papers, 1999, pages 49-52. Eklund, J-E. and Gustafsson, F., "Digital Offset Compensation of Time-Interleaved ADC Using Random Chopper Sampling," IEEE International Symposium on Circuits and Systems, IEEE, May AN1 28-31, 2000, pages III-447 thru III-450. DATE CONSIDERED **EXAMINER**

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance

and not considered. Include copy of this form with next communication to Applicant. 367818 1.DOC

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	AF2	WO 01/54317	07/2001	wo				N/A
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	AH2	WO 01/84702	11/2001	wo				N/A
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AH5 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Agazzi, O. et al., 10 Gb/s PMD Using PAM-5 Trellis Coded Modulation, IEEE 802.3, Alburquerque, March 6-10, 2000, 38 pages. AJ5 Bhatt, V., Equalization Ad Hoc Concluding Report, IEEE P802.3ae Plenary, March 2001, 12 pages. AK5 Bingham, J.A.C., "Multicarrier Modulation for Data Transmission: An Idea Whose Time Has Come," IEEE Communications Magazine, IEEE, Vol. 28, No. 5, pages 5-8 and 11-14. AL5 Chiddix, J. et al., "AM Video on Fiber in CATV Systems: Need and Implementation," IEEE Journal on Selected Areas in Communications, IEEE, Vol. 8, No. 7, September 1990, pages 1229-1239. Darcie, T., "Subcarrier Multiplexing for Lightwave Networks and Video Distribution Systems," IEEE Journal on Selected Areas in Communications, IEEE, Vol. 8, No. 7, September 1990, pages 1240-1248. AN5 Fettweis, G., "High-Rate Viterbi Processor: A Systolic Array Solution," IEEE Journal on Selected Areas in Communications, IEEE, Vol. 8, No. 8, October 1990, pages 1520-1534. EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.		AG5						No			
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